

Docket No.: GR 00 P 1579

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Date: July 23, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

By *J. P.*
JUL 30 2002
PATENT & TRADEMARK OFFICE
Applicant : Jenoe Tihanyi
Applic. No. : 09/815,657
Filed : March 23, 2001
Title : Field-Effect-Controllable Semiconductor Configuration With A Laterally Extending Channel Zone
Art Unit : 2822

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,844,273 (Konishi), dated December 1, 1998;

B. Jayant Baliga et al.: "The Accumulation-Mode Field-Effect Transistor: A New Ultralow On-Resistance MOSFET", *IEEE Electron Device Letters*, Vol. 13, No. 8, August 1992, pp. 427, 428.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,



For Applicant

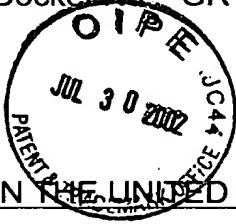
Mark P. Weichselbaum
Reg. No. 43,248

Date: July 22, 2002

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ASSOCIATE POWER OF ATTORNEY

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

Please recognize MARK P. WEICHSELBAUM (Reg. No. 43,248) as my associate in the matter in the above-identified application, with full powers. Please continue addressing all communications to the following address:

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Respectfully submitted,

For Applicant

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